In the Claims:

No amendments to the claims are presented.

1. (Previously presented) An interface integrated circuit device for interfacing a universal serial bus (USB) connection to a further circuit, the interface integrated circuit comprising:

first terminals for connecting to a USB bus;

a transceiver capable of transceiving for both a USB host and a USB device, the transceiver having a USB interface, a host interface and a device interface, the USB interface being coupled to the first terminals;

second terminals coupled to the device interface for connection to an external USB device controller;

a host controller coupled to the host interface, the host controller having a parallel data/address bus;

third terminals coupled to the parallel data/address bus.

2. (Original) An integrated circuit device according to claim 1, wherein the device interface comprises both an analog USB device interface and a transceived digital USB device interface for connection to an external USB device controller without and with an external transceiver, respectively.

3. (Previously presented) An electronic apparatus with a universal serial bus (USB) connection, the electronic apparatus comprising:

a functional circuit with a processor, a parallel address data bus coupled to the processor and a USB device controller circuit with a USB interface in parallel with said address/data bus; and

an interface integrated circuit electronically between the USB connection on the one hand and the parallel address/data bus and the USB interface on the other hand, the interface integrated circuit including:

first terminals for connecting to the USB connection;

a transceiver capable of transceiving for both a USB host and a USB device, the transceiver having a USB interface, a host interface and a device interface, the USB interface being coupled to the first terminals; the device interface being connected to the USB device controller circuit in said functional circuit; and

a host controller coupled to the host interface, the host controller being coupled to the processor via the parallel data/address bus.

4. (Original) An electronic apparatus according to claim 3, wherein the apparatus is arranged to use USB communication from said host controller via the USB connection in a first speed mode when operating as USB host and to use USB communication via the USB connection in a second speed mode, different from said first speed mode, as determined by the device controller when operating as USB device.

5. (Previously presented) An electronic system comprising:

a universal serial bus (USB) bus connection;

a host apparatus and a device apparatus, at least one of the host and the device apparatus including a functional circuit with a processor, a parallel address data bus coupled to the processor and a USB device controller circuit with a USB interface in parallel with said address/data bus; and

an interface integrated circuit electronically between the USB bus connection on the one hand and the parallel address/data bus and the USB interface on the other hand, the interface integrated circuit including:

first terminals for connecting to ~~a~~ the USB bus connection;

a transceiver capable of transceiving for both a USB host and a USB device, the transceiver having a USB interface, a host interface and a device interface, the USB interface being coupled to the first terminals; the device interface being connected to the USB device controller circuit;

a host controller coupled to the host interface, the host controller being coupled to the processor via the parallel data/address bus.

6. (Previously presented) A method of operating an interface integrated in a universal serial bus (USB) system, the method comprising:

receiving a selection whether the apparatus containing the interface integrated circuit should operate as a USB host or as a USB device;

transceiving USB signals with a transceiver in the interface integrated circuit;

sequencing USB communication via the transceiver with a host controller in the interface integrated circuit and communicating USB transceived data to or from functional circuits outside the integrated circuit via a parallel address data interface when USB host operation is selected, and passing USB signals from the transceiver to a device controller in the functional circuits outside the integrated circuit when USB device controller operation is required.

7. (Previously Presented) A method according to claim 6, wherein passing USB signals from the transceiver to the device controller in the functional circuits outside the integrated circuit includes passing analog USB signals from the transceiver to the device controller when the device controller includes a transceiver and passing transceived digital USB signals from the transceiver to the device controller when the device controller does not include a transceiver.

8. (Previously Presented) A method according to claim 6, wherein the interface integrated circuit does not include a USB device controller.

9. (Previously Presented) An interface integrated circuit device according to claim 1, wherein the interface integrated circuit does not include a USB device controller.

10. (Previously Presented) An interface integrated circuit device according to claim 1, wherein the interface integrated circuit is arranged to use USB communication from said host controller via the USB connection in a first speed mode when operating as a USB host and to use USB communication via the USB connection in a second speed mode, different from said first speed mode, as determined by the device controller when operating as a USB device.

11. (Previously Presented) An electronic apparatus according to claim 3, wherein the device interface includes both an analog USB device interface and a transceived digital USB device interface for connection to the USB device controller circuit without and with an external transceiver, respectively.

12. (Previously Presented) An electronic apparatus according to claim 3, wherein the interface integrated circuit does not include a USB device controller.

13. (Previously Presented) An electronic system according to claim 5, wherein the device interface includes both an analog USB device interface and a transceived digital USB device interface for connection to the USB device controller circuit without and with an external transceiver, respectively.

14. (Previously Presented) An electronic system according to claim 5, wherein the interface integrated circuit does not include a USB device controller.

15. (Previously Presented) An electronic system according to claim 5, wherein the system is arranged to use USB communication from said host controller via the USB connection in a first speed mode when operating as a USB host and to use USB communication via the USB connection in a second speed mode, different from said first speed mode, as determined by the device controller when operating as a USB device.